Here is a possible correct output for the Mutex LockTester. Notice that the threads are each running in a random pattern. It is a problem if thread A runs 10 times, then thread B runs 10 times, etc. Their execution should be interleaved. However, each line should be consecutively numbered from 1 to 70. Example Thread-based Programs... Initializing Thread Scheduler... -- You should see 70 lines, each consecutively numbered. --LockTester-A = 1LockTester-A = 2LockTester-A = 3LockTester-C = 4LockTester-C = 5LockTester-C = 6LockTester-C = 7LockTester-C = 8LockTester-A = 9LockTester-D = 10LockTester-D = 11LockTester-D = 12LockTester-D = 13LockTester-E = 14LockTester-B = 15LockTester-E = 16LockTester-C = 17LockTester-C = 18LockTester-C = 19LockTester-C = 20LockTester-C = 21LockTester-E = 22LockTester-A = 23LockTester-A = 24LockTester-A = 25LockTester-G = 26LockTester-G = 27LockTester-G = 28LockTester-G = 29LockTester-G = 30LockTester-A = 31LockTester-A = 32LockTester-B = 33LockTester-A = 34LockTester-E = 35LockTester-D = 36LockTester-D = 37LockTester-E = 38LockTester-F = 39LockTester-F = 40LockTester-F = 41LockTester-F = 42LockTester-F = 43LockTester-F = 44LockTester-F = 45LockTester-F = 46LockTester-F = 47

LockTester-F = 48

LockTester-G	=	49
LockTester-B	=	50
LockTester-G	=	51
LockTester-G	=	52
LockTester-G	=	53
LockTester-G	=	54
LockTester-D	=	55
LockTester-D	=	56
LockTester-B	=	57
LockTester-E	=	58
LockTester-D	=	59
LockTester-D	=	60
LockTester-B	=	61
LockTester-E	=	62
LockTester-B	=	63
LockTester-E	=	64
LockTester-B	=	65
LockTester-E	=	66
LockTester-B	=	67
LockTester-E	=	68
LockTester-B	=	69
LockTester-B	=	70

***** A 'wait' instruction was executed and no more interrupts are scheduled... halting emulation *****